

Senior/Staff Hardware Engineer

Location: San Jose, Ca

JOB ID: SrHardwareEng

Description:

Looking for self-motivated, team oriented senior / Staff Hardware Design engineer to be a part of the Nixsun Hardware & System development team for the next generation leading edge Network Security platform. Apply your extensive product development experience and lead projects from concept to manufacturing release. Leverage your design skills in high-speed PCB, ASIC I/O, high speed SerDes, signal integrity, power integrity and develop cost optimized, high volume products. Perform thorough upfront analysis and design reviews to get it right the first time. Commanding breadth of experience in product development. Hands-on experience designing, testing, and releasing to manufacturing a minimum of 3 products. Solid understanding and experience designing low cost, high speed PCBs. Experience following a product development process and meeting aggressive objectives.

Requirements:

- High Performance, high speed board design experience.
- Working knowledge of Hardware Design tools for schematic capture, signal integrity and timing analysis.
- Hands on experience with at least one of the high speed interfaces / buses such as SPI4.x, PCI-Express, Infiniband etc.
- Ability to write Design architecture, Functional and Design specifications.
- Define Diagnostic requirements.
- Experience with Board Layout and ability to write layout rules for working with outside CAD Layout designers
- Experience working with the Tier1 – Tier3 Contract Manufacturing companies and producing designs that meet DFT and DFA requirements
- Ability to work with FPGA engineers.
- Small FPGA/CPLD design experience
- Good understanding of the networking protocols such as ATM and IP
- Experience with networking devices, including routers, switches, taps, repeaters, converters is a plus.
- Typically requires BS/MS in CS/EE with 8+ years of relevant experience in high speed digital design and technologies.

Preferred Skills:

- Hands on experience using ORCAD, Mentor Graphics View Logic or Cadence Design Concept schematic capture tools
- Recent design experience with PCI and PCI-Express interfaces and chips
- 10Gigabit interface design is a plus
- Knowledge and experience with Network Processor based design
- Good Bring up and test experience
- Storage system design experience is plus
- Strong interpersonal and communication skills; Independent problem solving; self- motivated.

Primary Duties:

- Work with cross functional teams and complete the definition of the next generation architecture.
- Complete design, schematic capture, board layout and assembly of the new line card.
- Participate in selecting technologies and components for the design
- Lead efforts to define and write diagnostic and test specifications.
- Hire hardware technician and complete test and bring up of the new design
- Effectively document technical issues - design restrictions, workarounds, and corrective actions.
- Provide regular status and progress towards milestones with program leaders, and senior engineering management.